

FIG. 1D

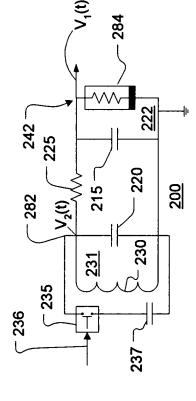
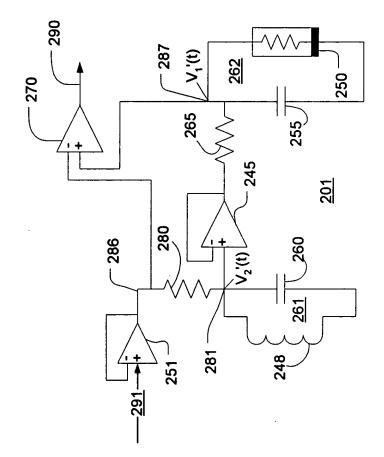
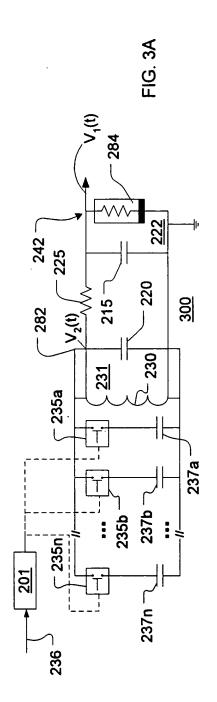


FIG. 2A

FIG. 2B (PRIOR ART)





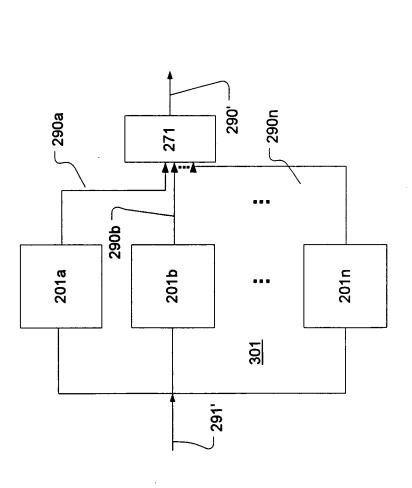
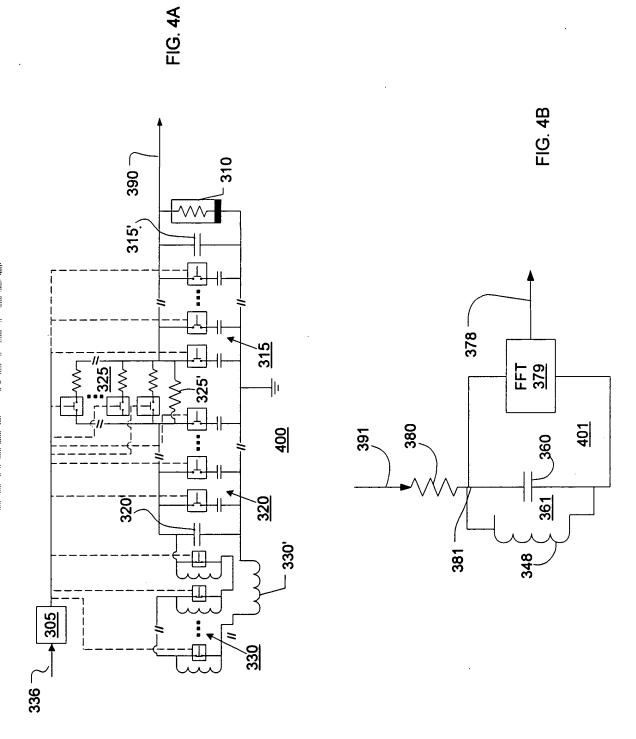


FIG. 3B



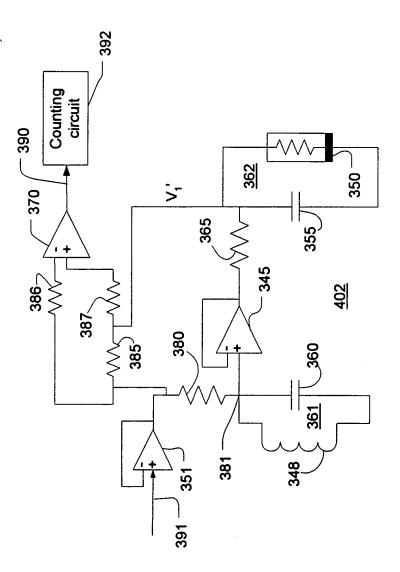


FIG. 40

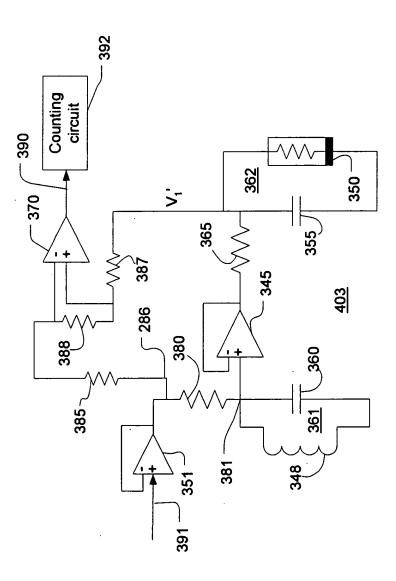


FIG. 41

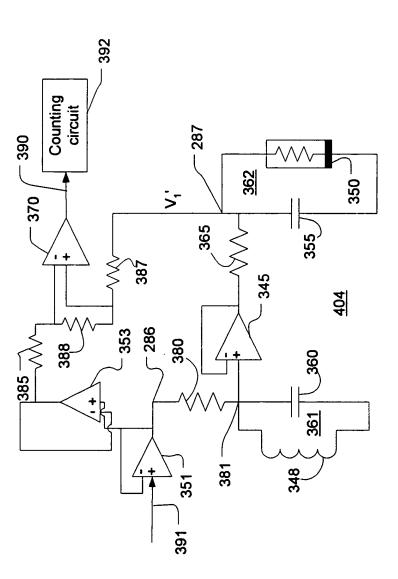


FIG. 4E

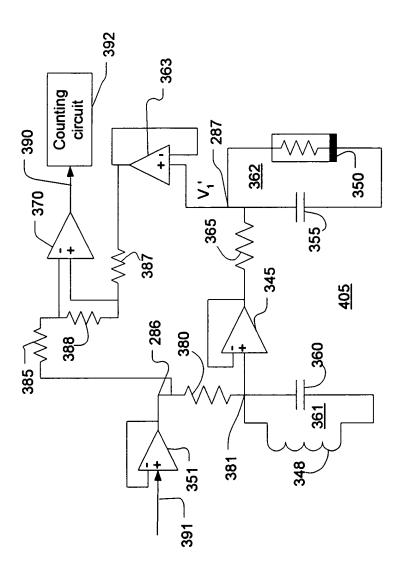


FIG. 4F

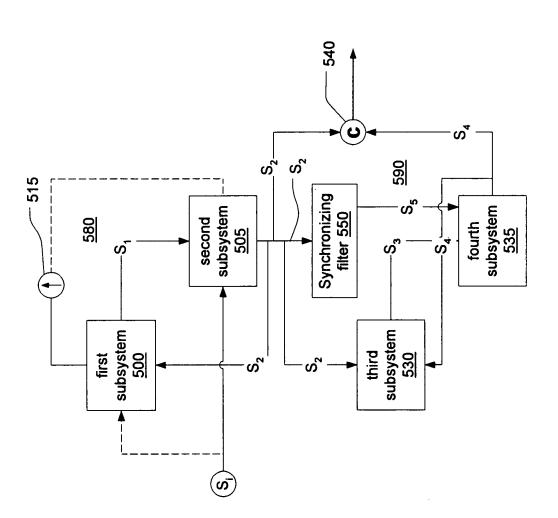


FIG. 5

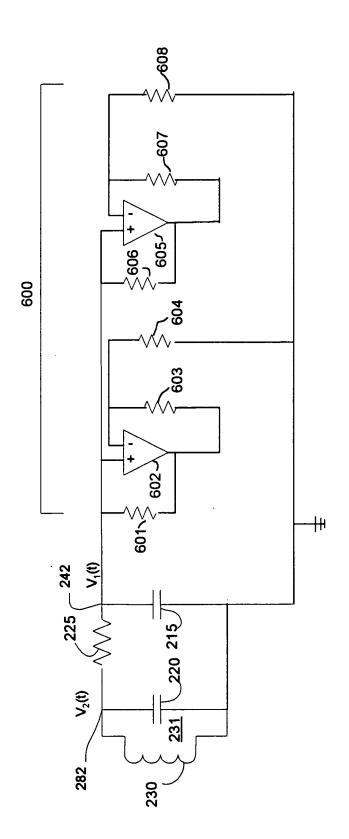


FIG. 6A (PRIOR ART)

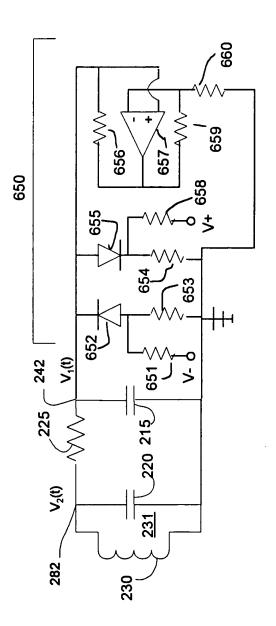


FIG. 6B (PRIOR ART)

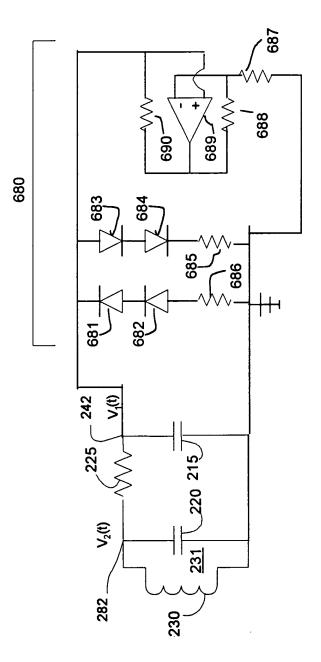


FIG. 6C

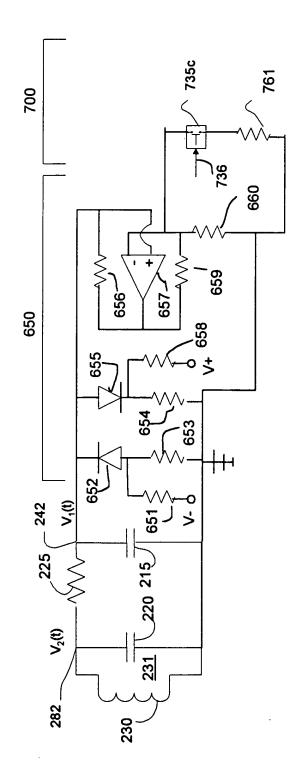


FIG. 7A

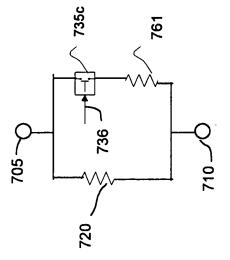


FIG. 7B

Fig 6b Caltech R2'		R2	R1	R2' / R2	R2' / R2 R2'/R2/R1
element	653	1 59)	660 653/654	653/654/660
Ga			х .		ж
Gb upper scroll		X	X	X	x
Gb lower scroll	X		×	X	×

Ga = -1/R1 Gb = (R1-R2)/(R1*R2)

209/809/909 R1/R2/R3 809/909 R1/R3 × × 88 × R3 R2 607 R1 606 × × 8 8 8 **3** 8 3 109 × × Fig 6a Kennedy Gb upper Gb lower element

603/604 606/607/608/601

R1/R2/R3/ R4

RS/R6

 $\begin{aligned} G_{a} &= -(R2/(R1*R3) - (R5/(R4*R6))) \\ Gb &= -(R2/(R1*R3) + (1/R4)) \end{aligned}$

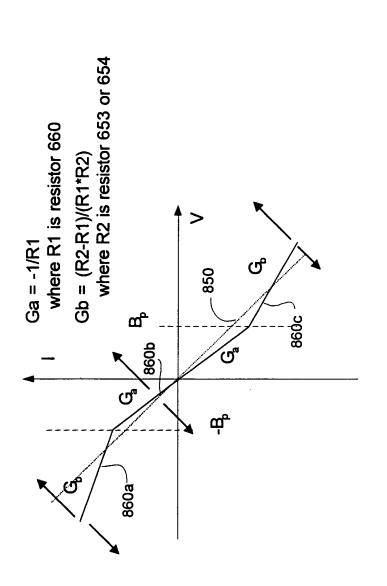
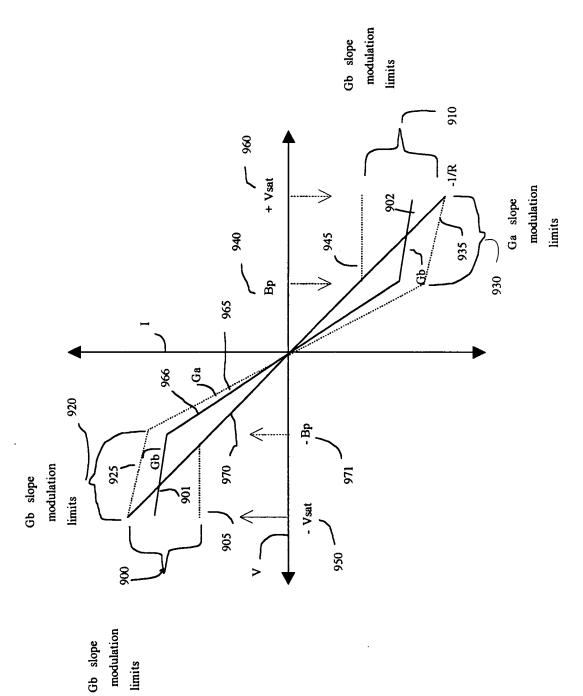


FIG. 8



limits

FIG. 9A

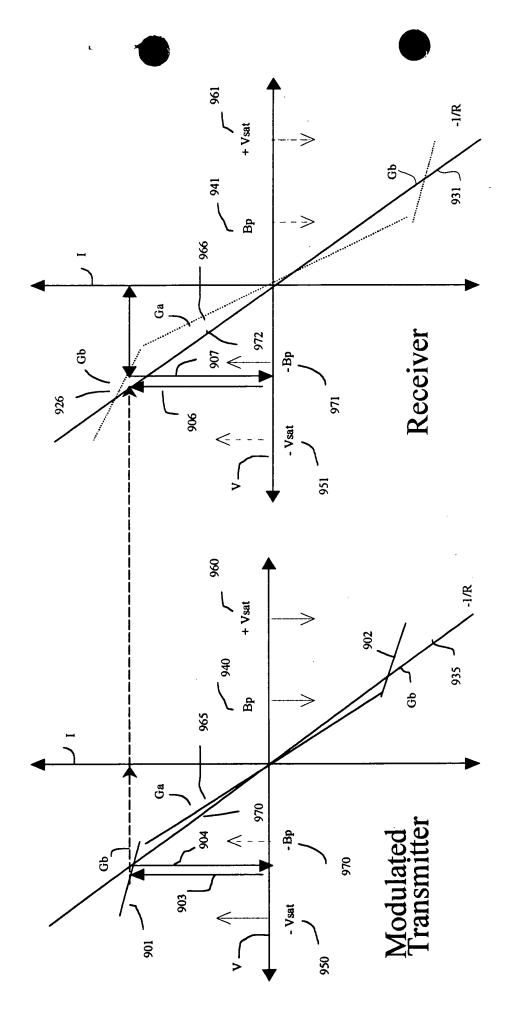


FIG. 9B

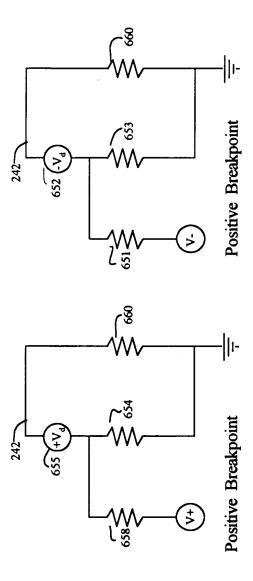


FIG. 9C

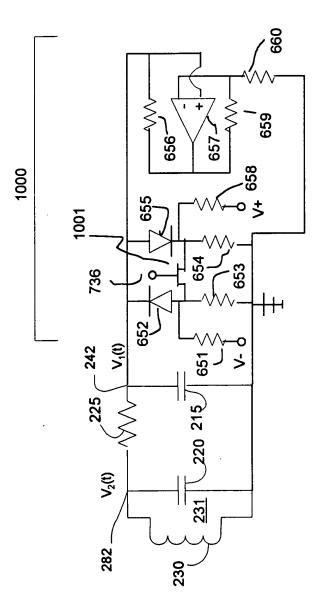


FIG. 10

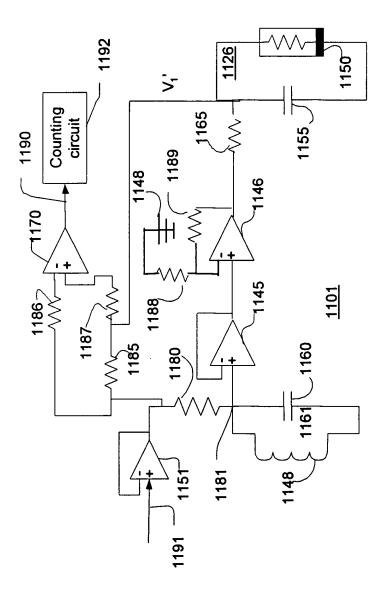


FIG. 11

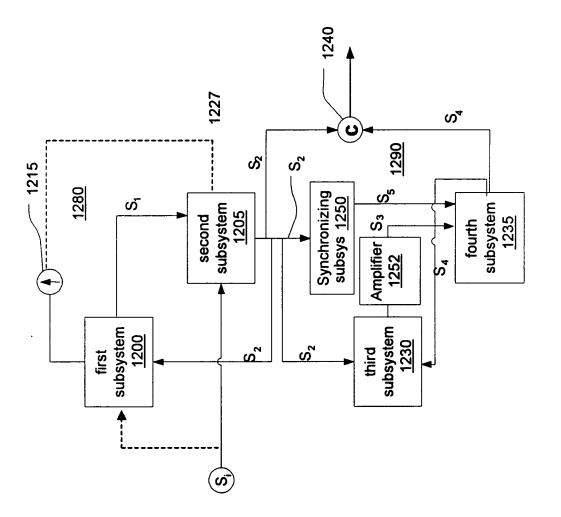


FIG. 12

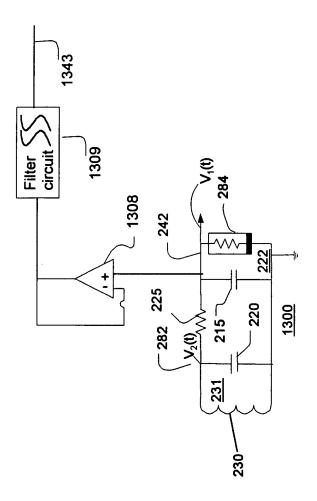
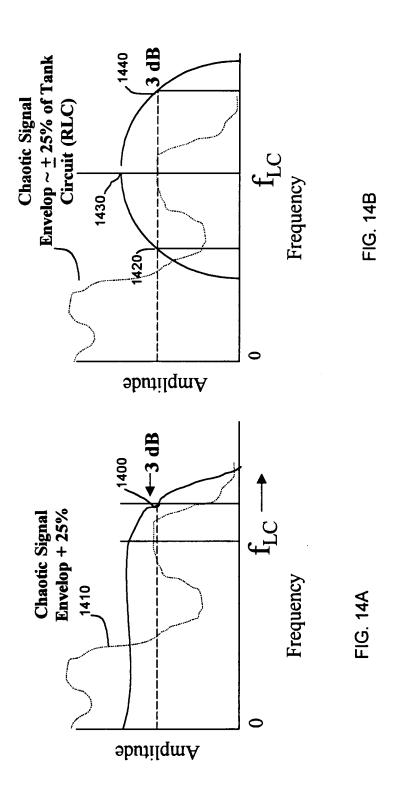


FIG. 13



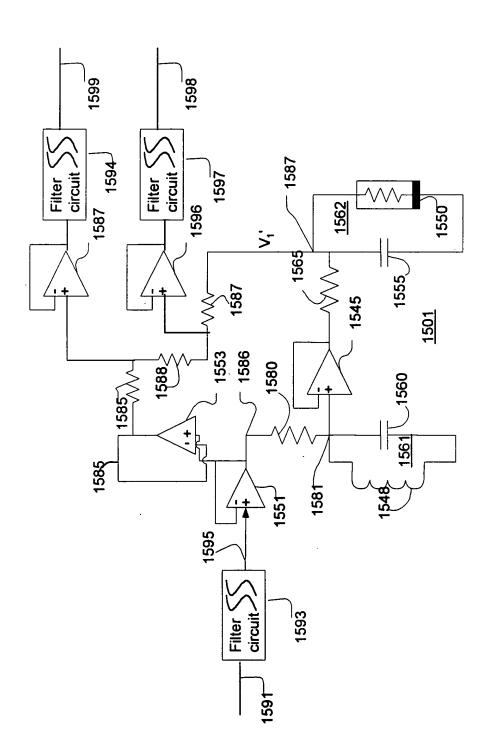


FIG. 15

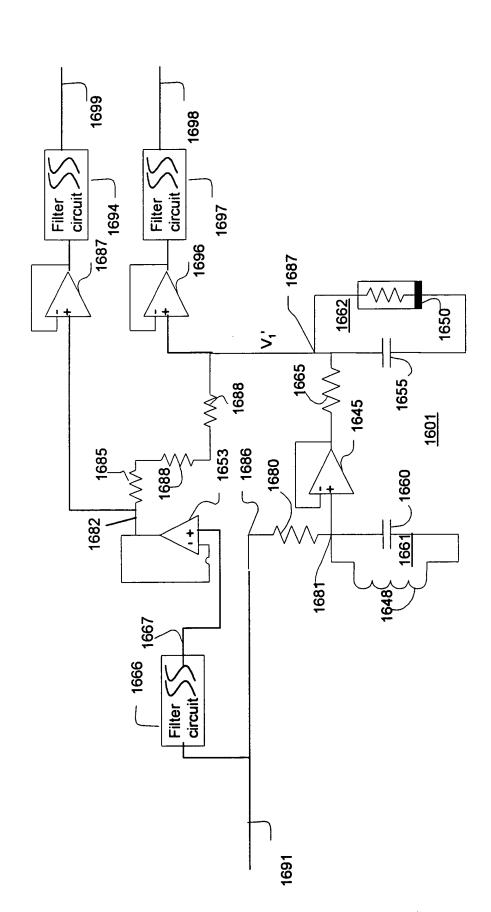


FIG. 16

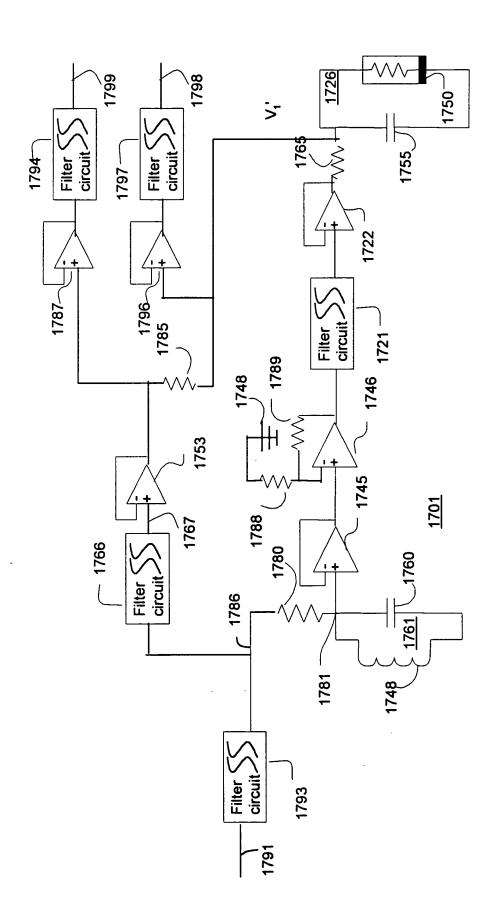
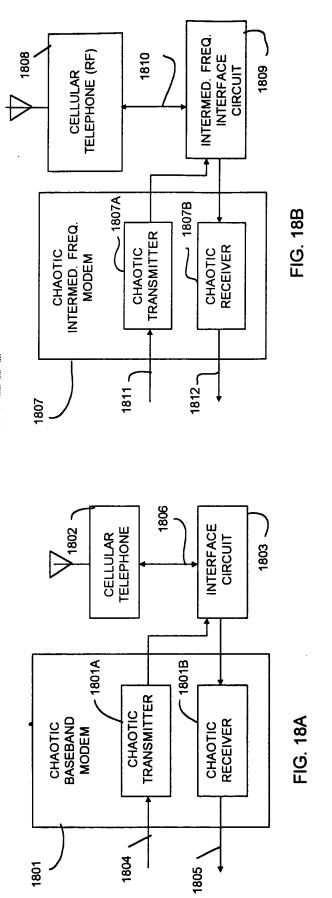


FIG. 17



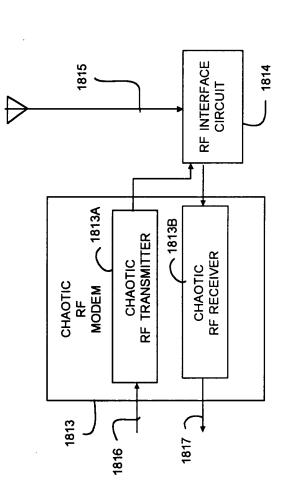
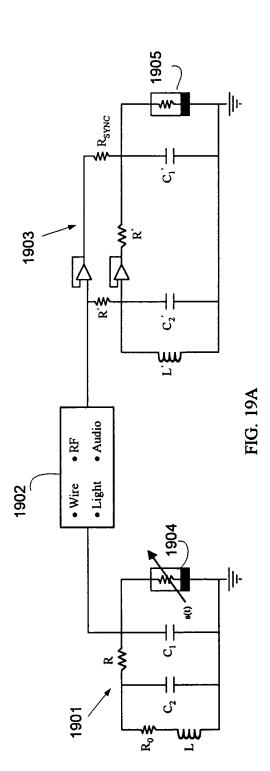


FIG. 18C



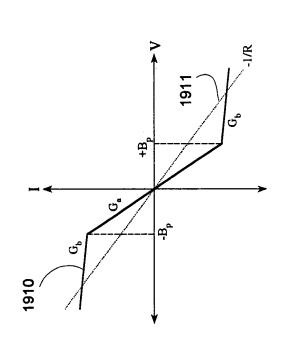
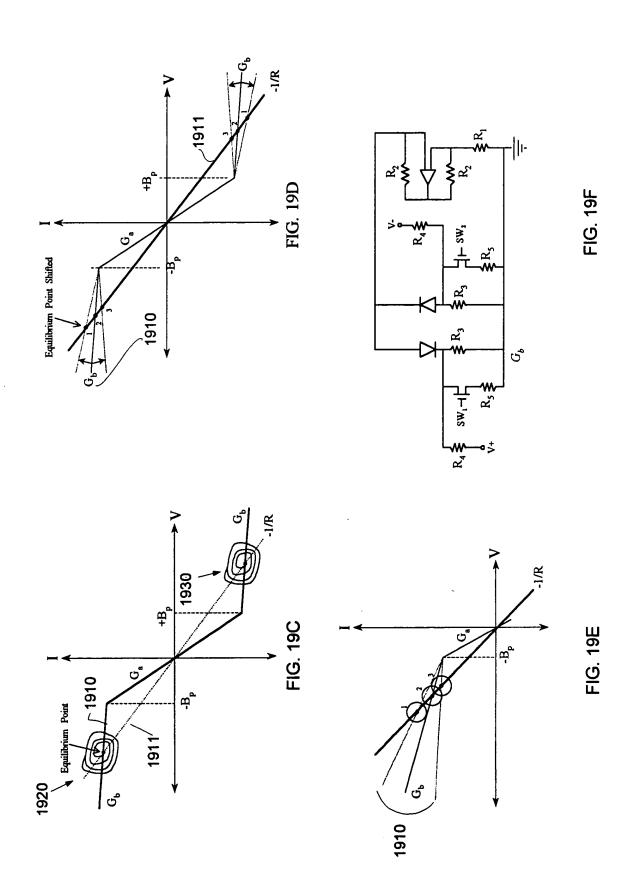


FIG. 19B



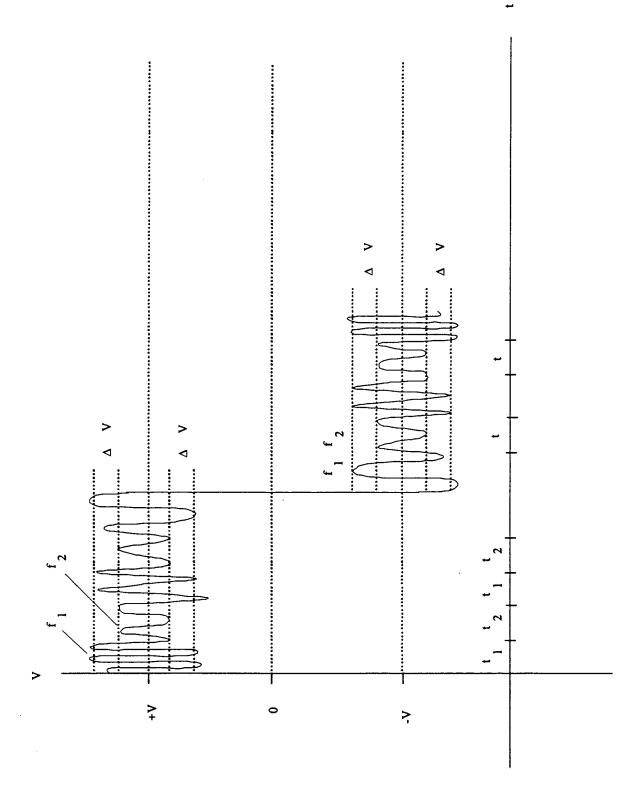
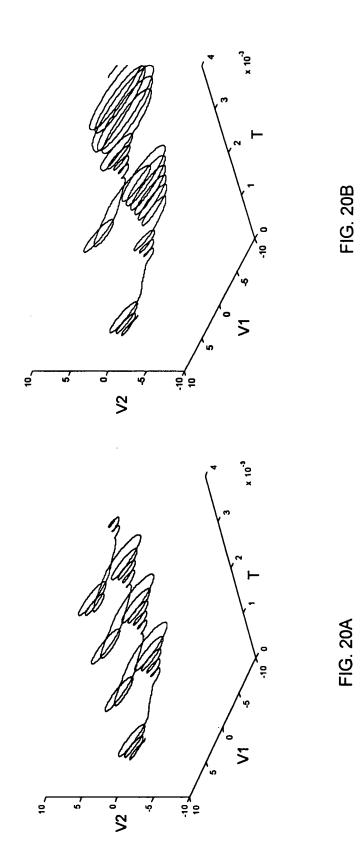
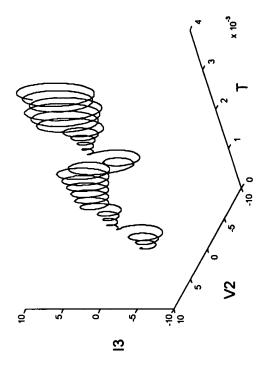
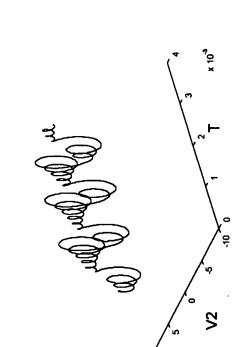


FIG. 19G



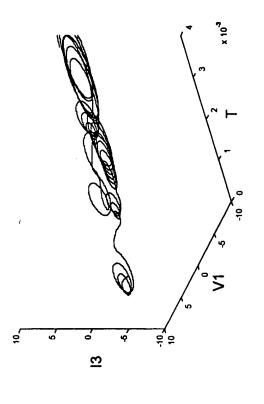




, 5

13°

FIG. 20D



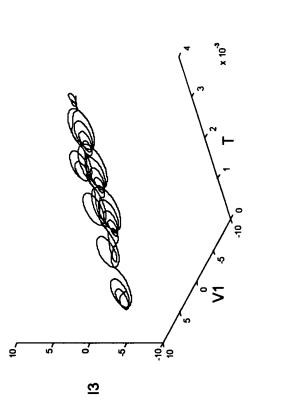


FIG. 20F

FIG. 20E

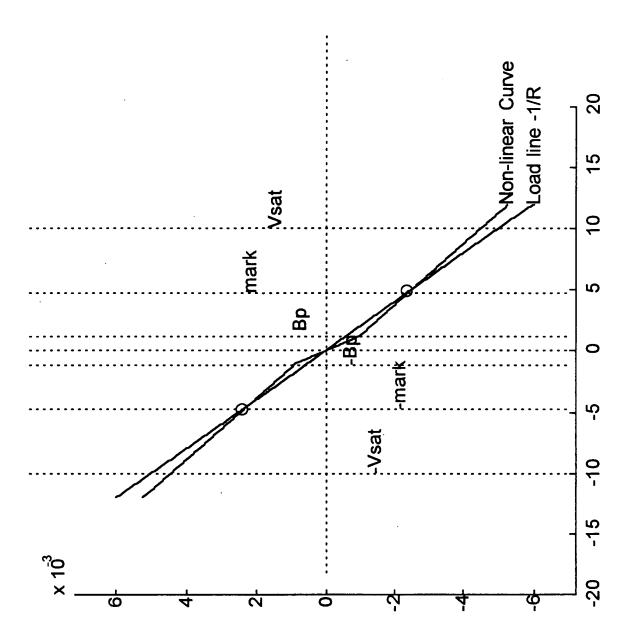


FIG. 21A

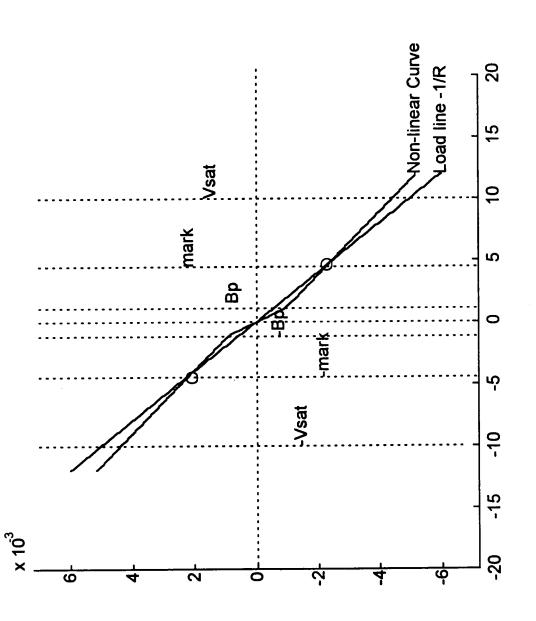
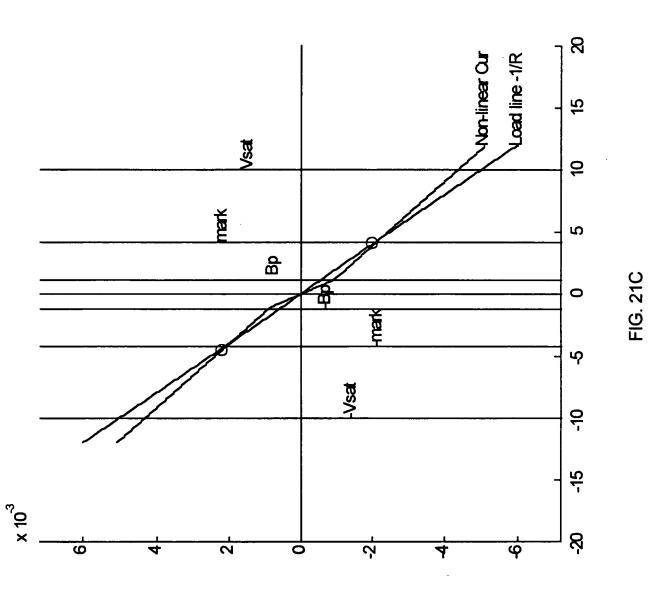


FIG. 21B



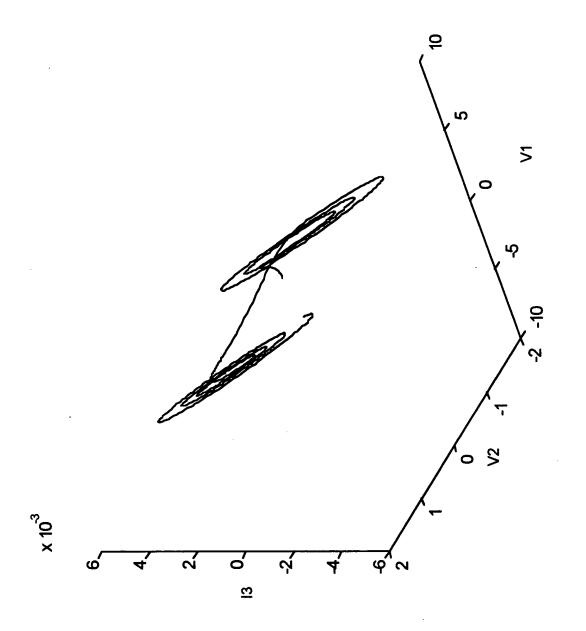


FIG. 21D

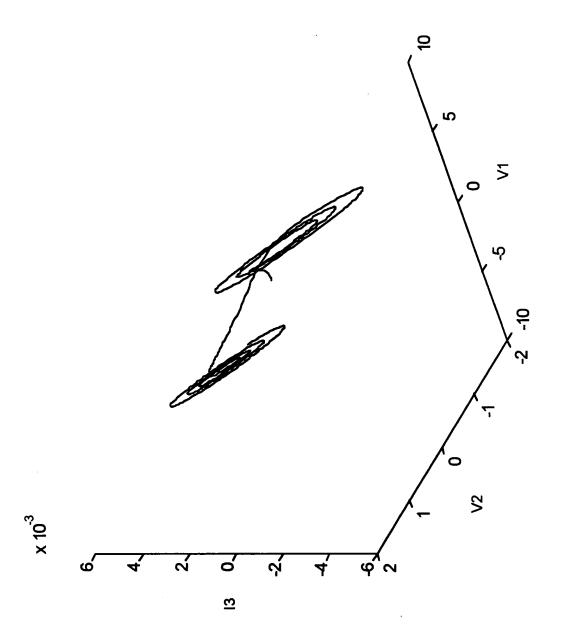


FIG. 21E

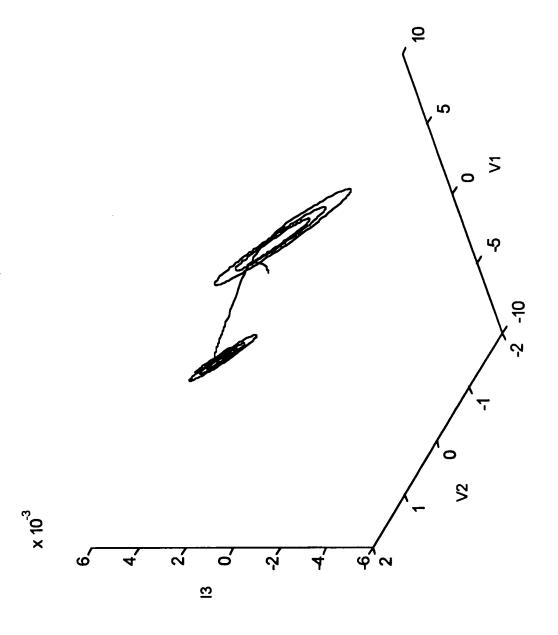
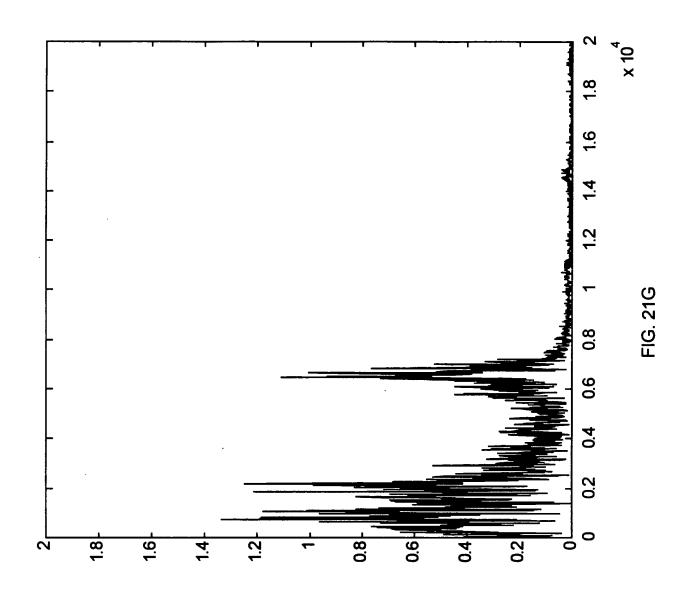
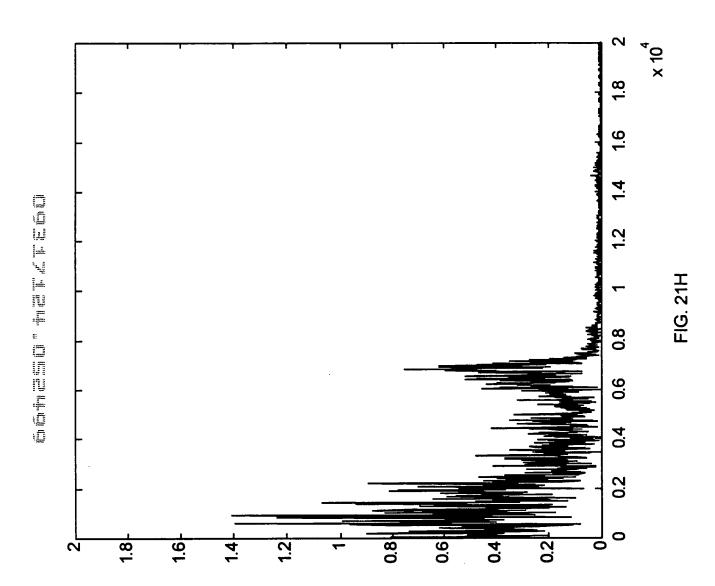
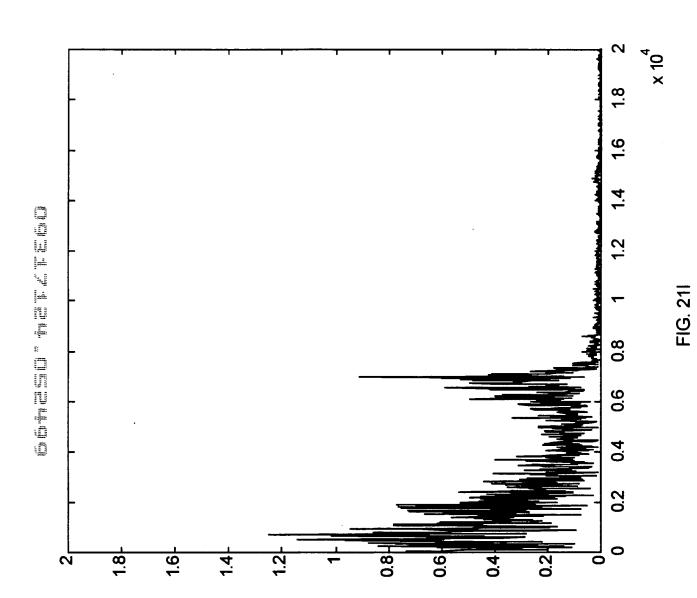


FIG. 21F







Modulation

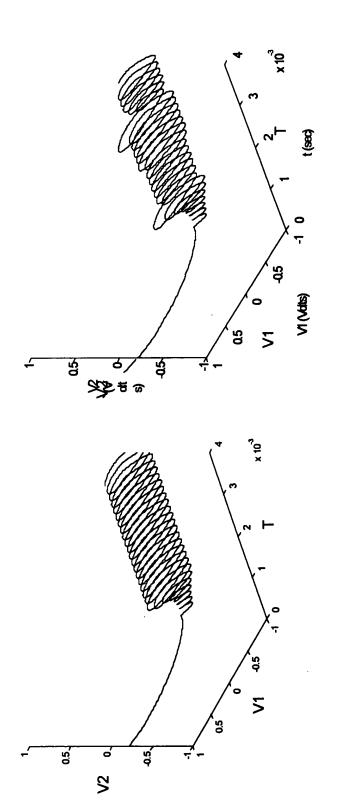
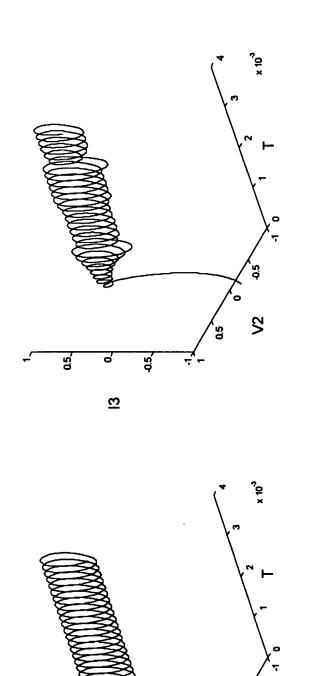


FIG. 22B

FIG. 22A



0.5

<u>ლ</u>

0.5

FIG. 22D

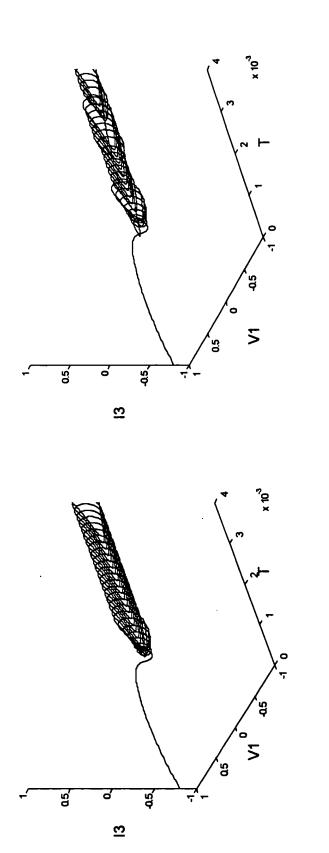


FIG. 22E

FIG. 22F

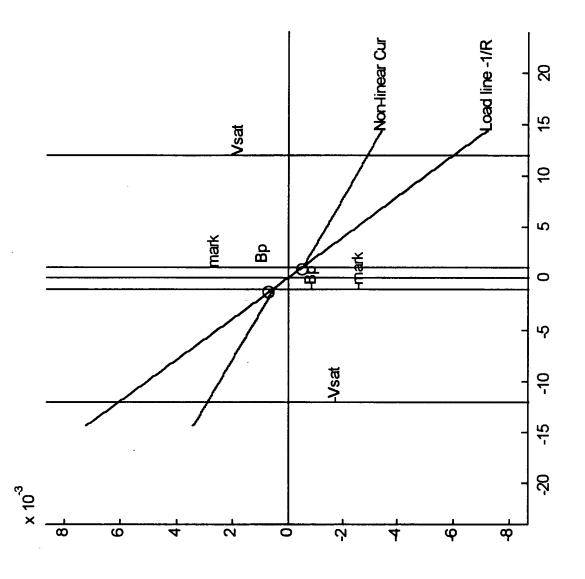
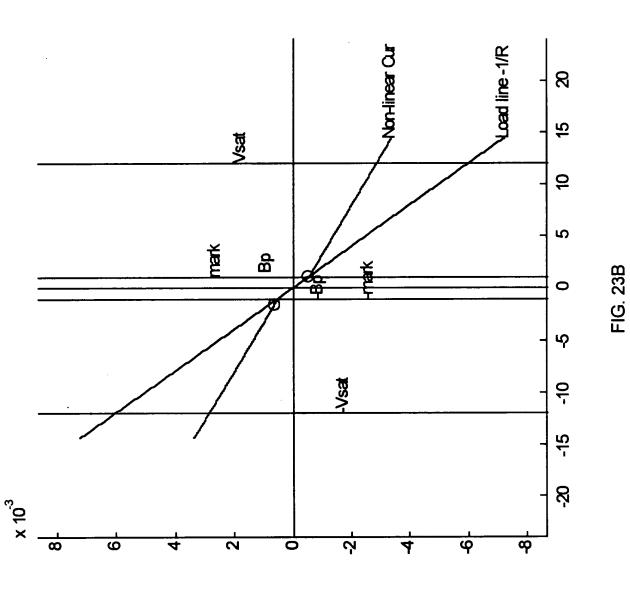
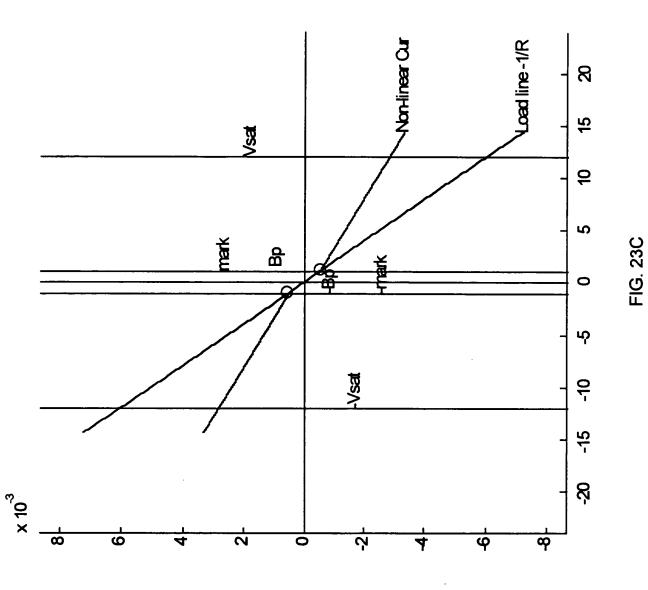


FIG. 23A





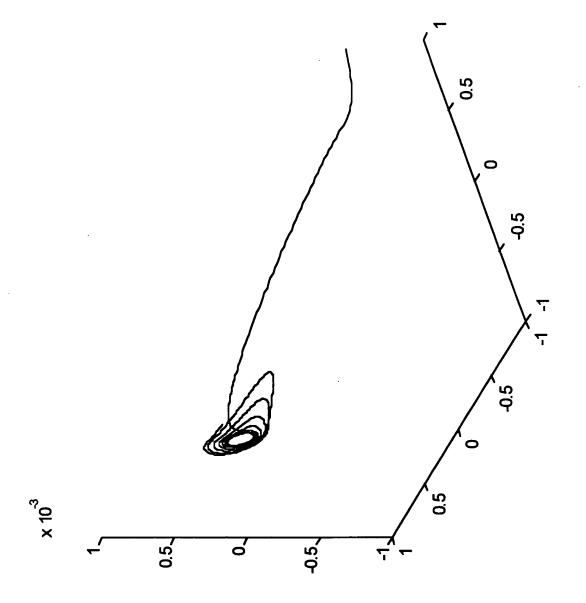


FIG. 23D

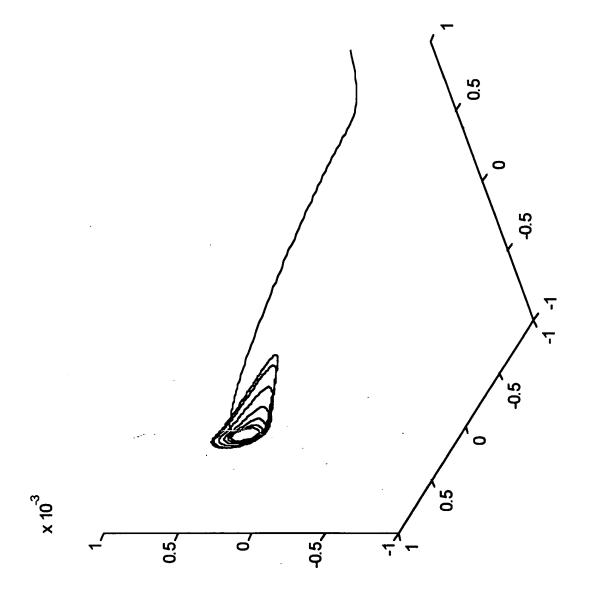


FIG. 23E

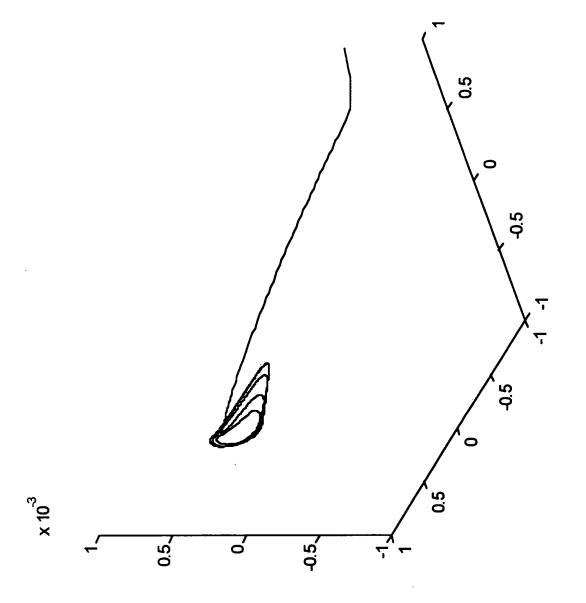


FIG. 23F

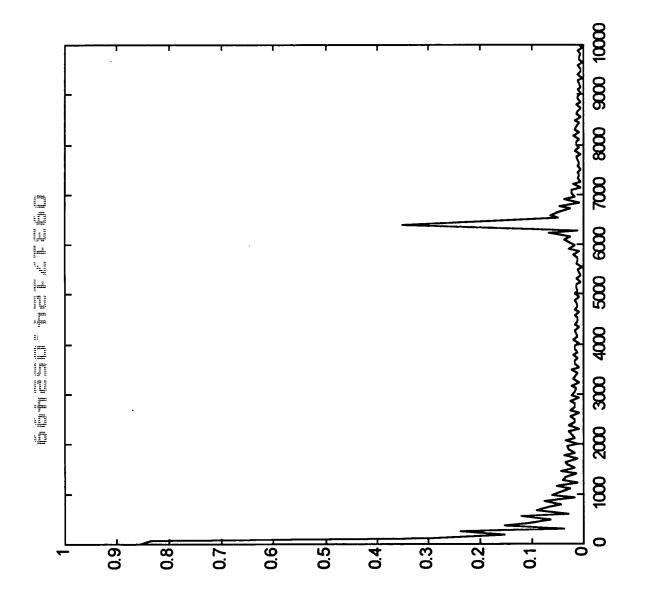


FIG. 23G

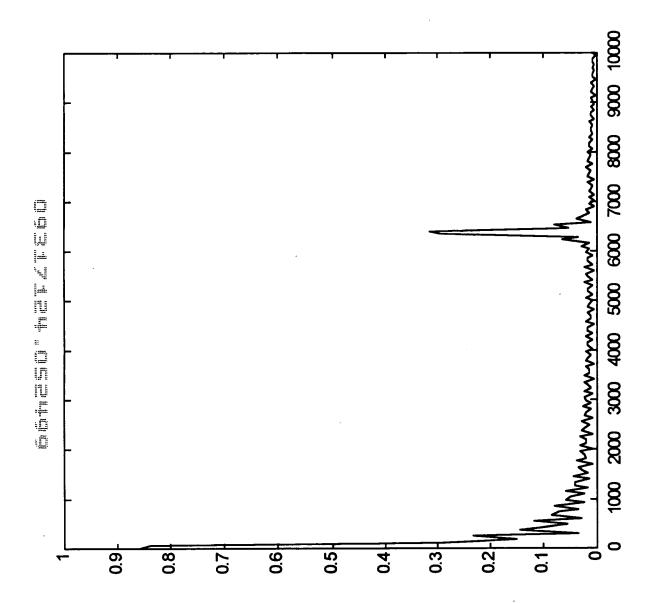


FIG. 23H

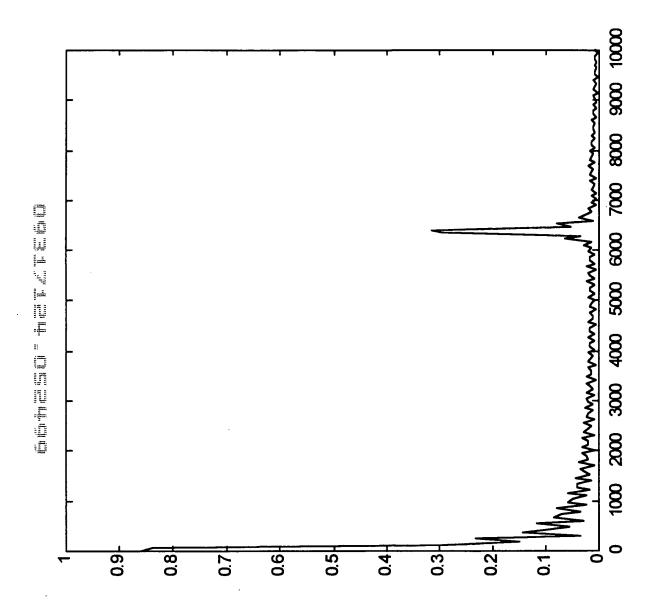


FIG. 231

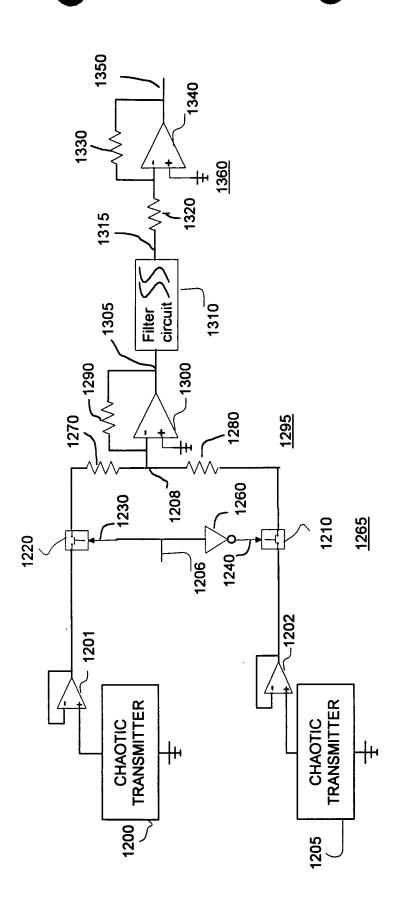
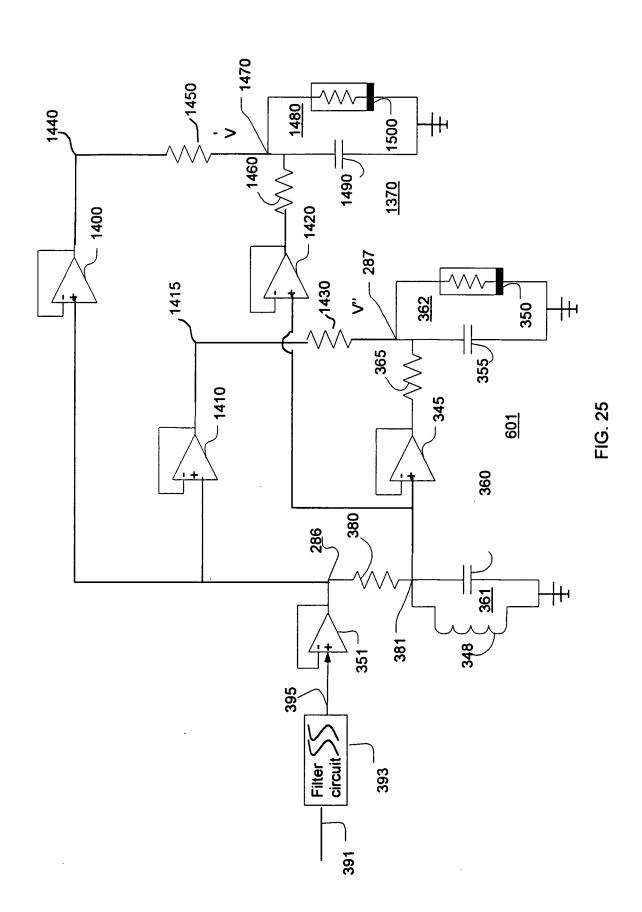


FIG. 24



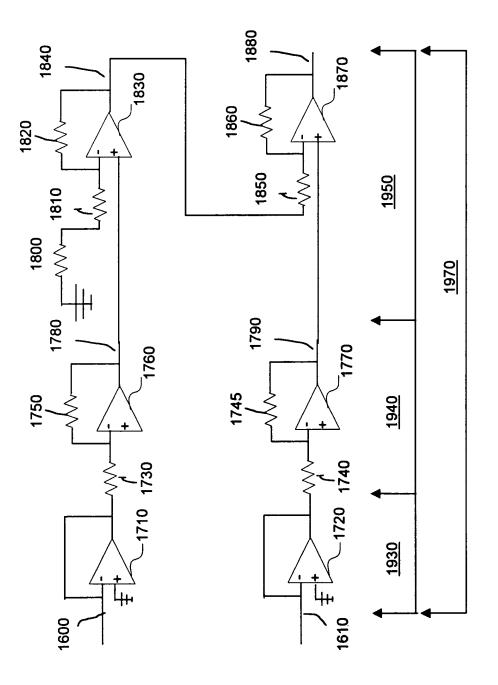


FIG. 26

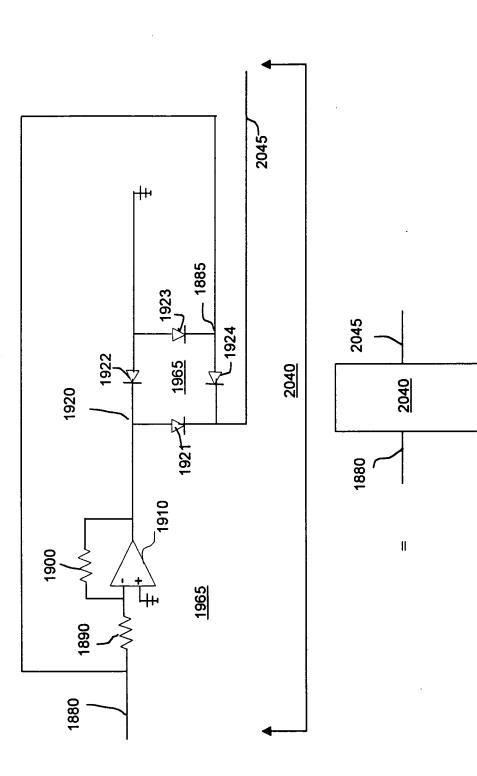


FIG. 27

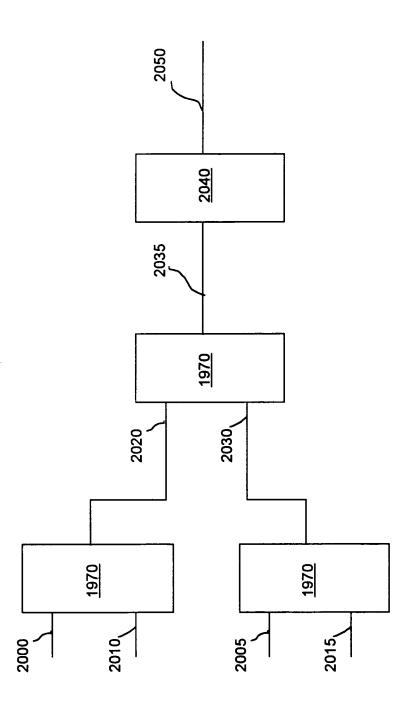


FIG. 28

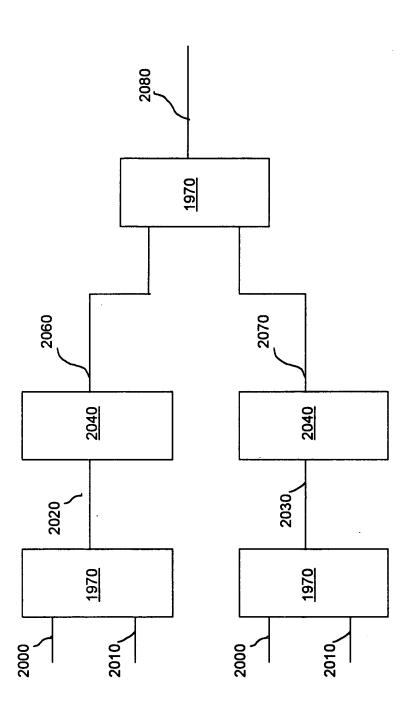


FIG. 29

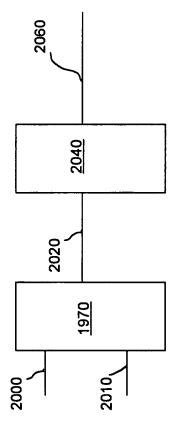


FIG. 30

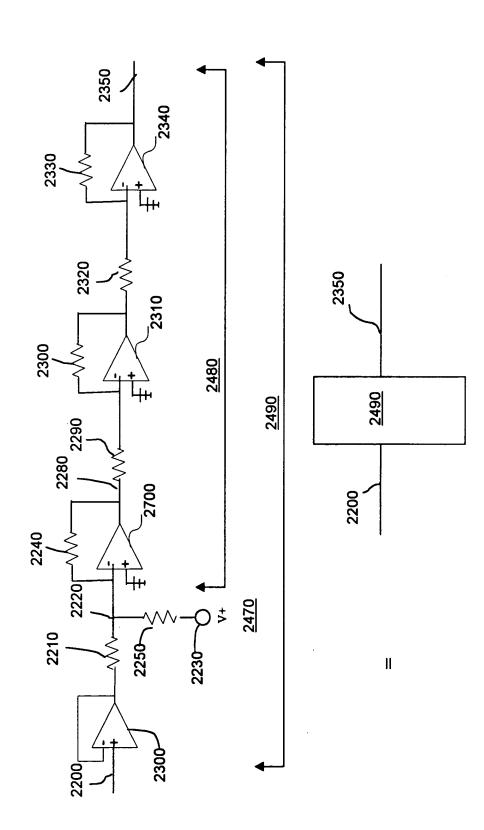


FIG. 31

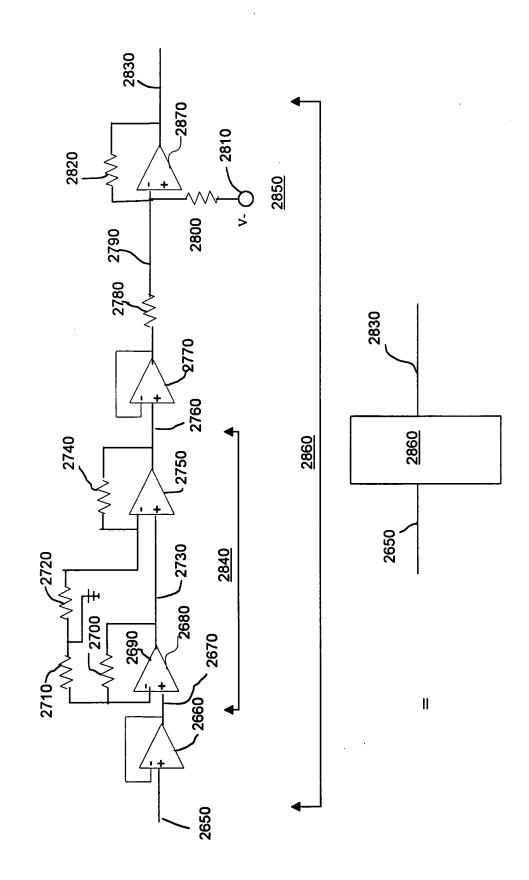


FIG. 32

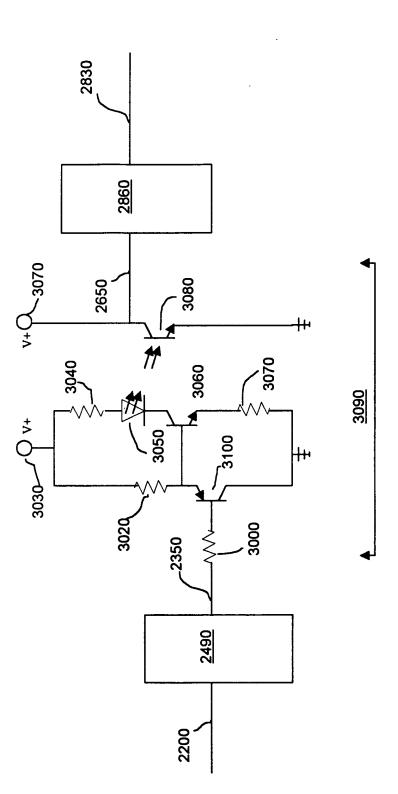


FIG. 33

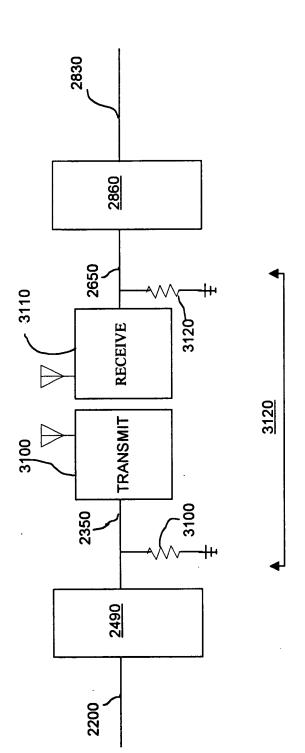


FIG. 34

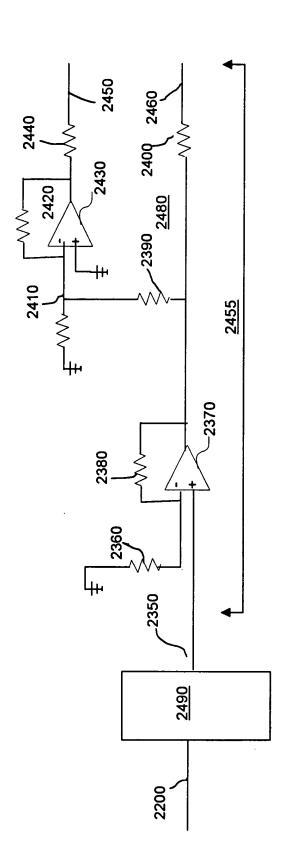


FIG. 35

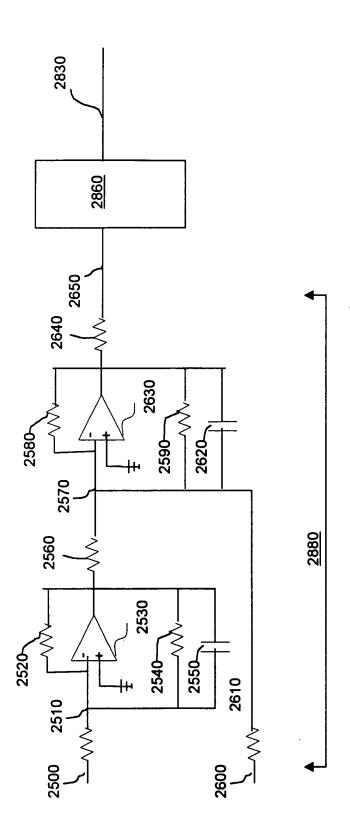
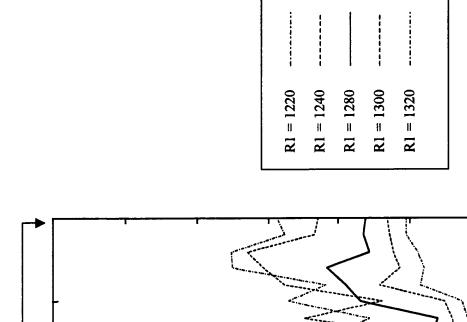


FIG. 36

Chaotic Operating Region

2100



Voltage Difference across Rsync [385] in volts



1400

FIG. 37A

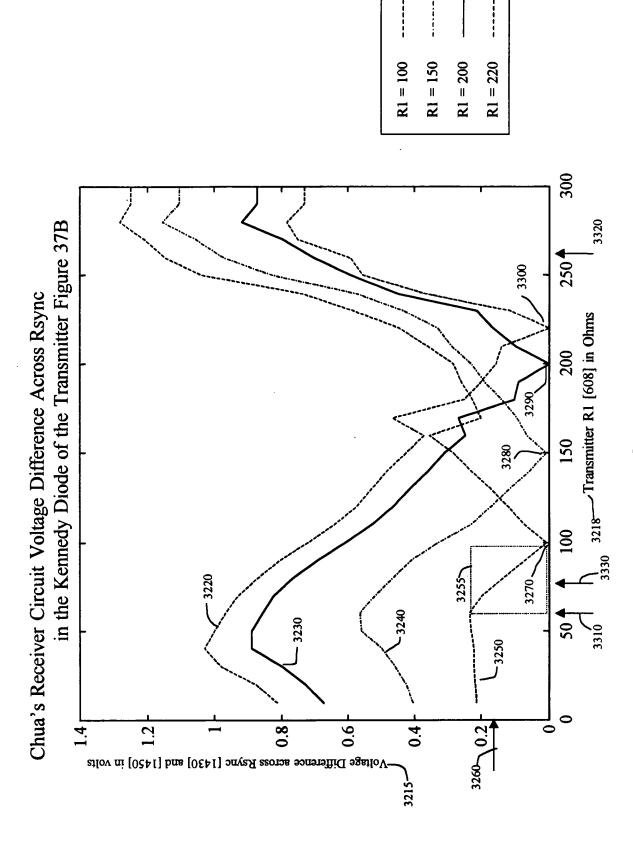
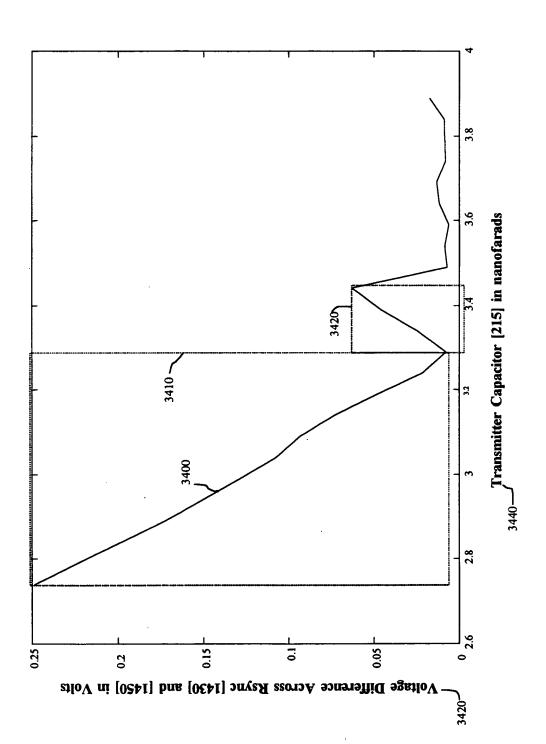


FIG. 37B



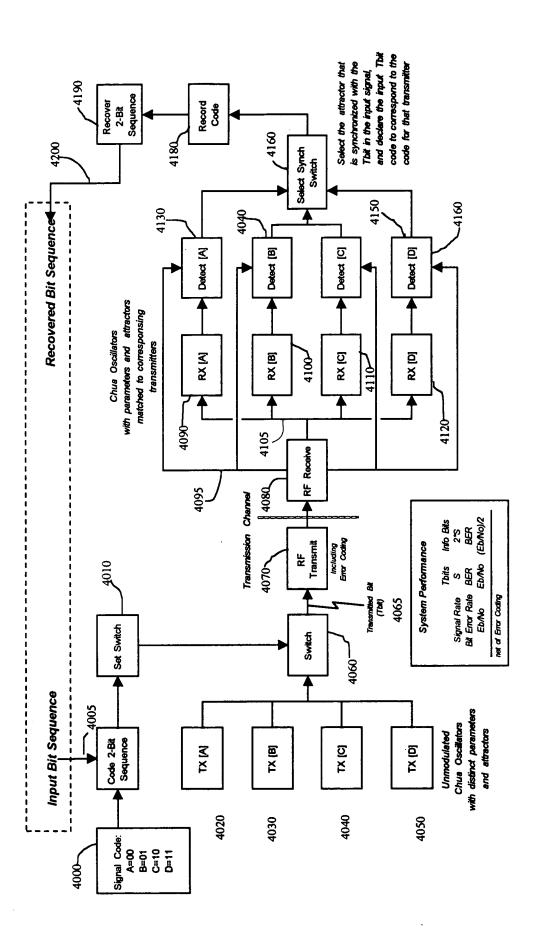


FIG. 38

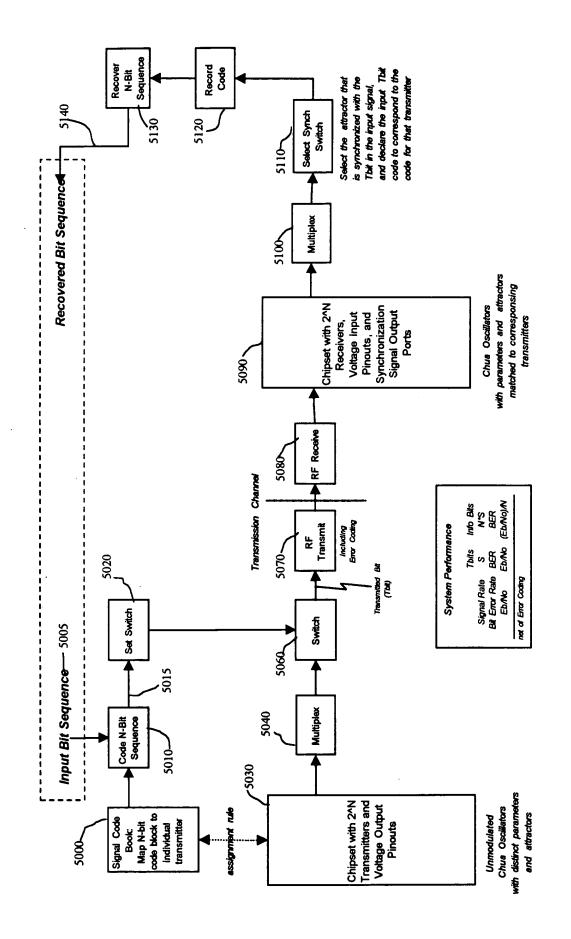


FIG. 39

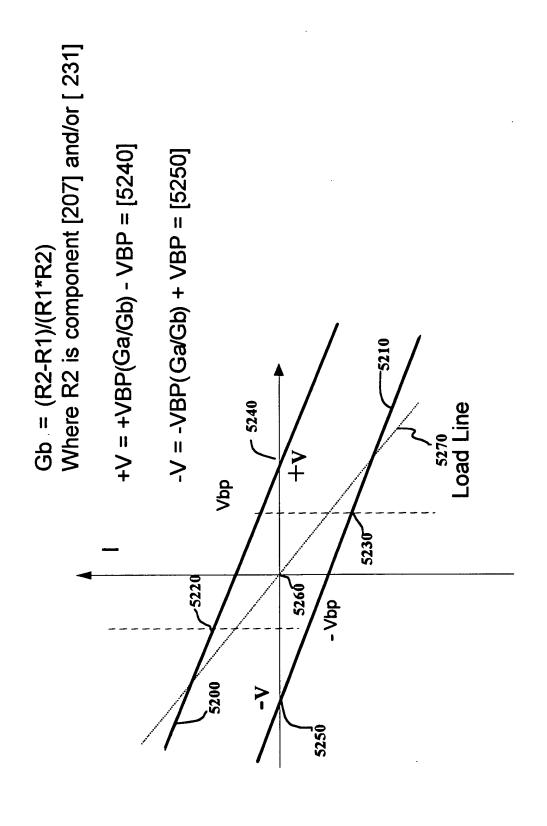


FIG. 40

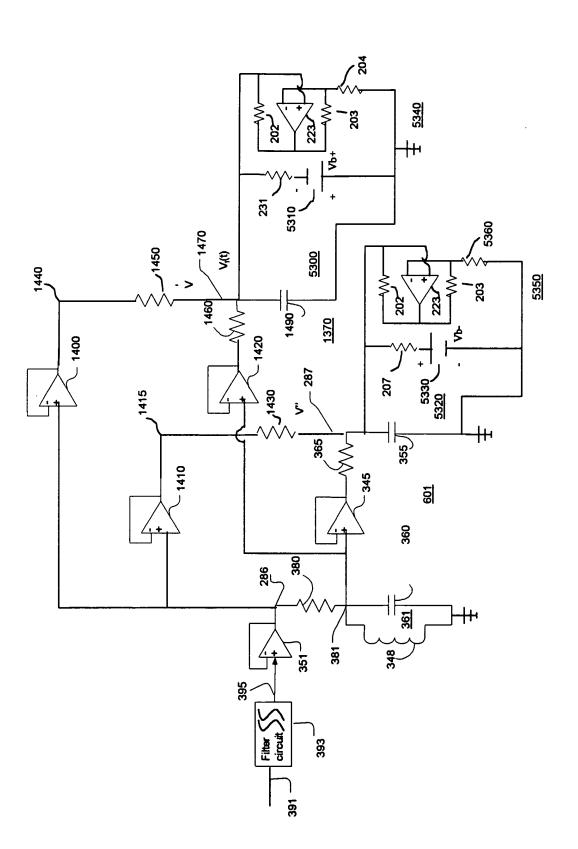


FIG. 41

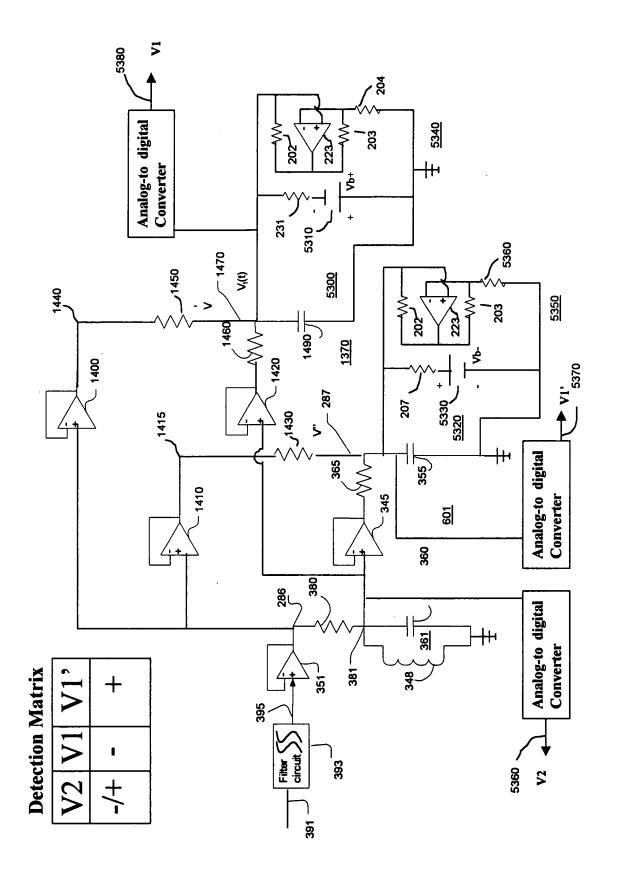


FIG. 42

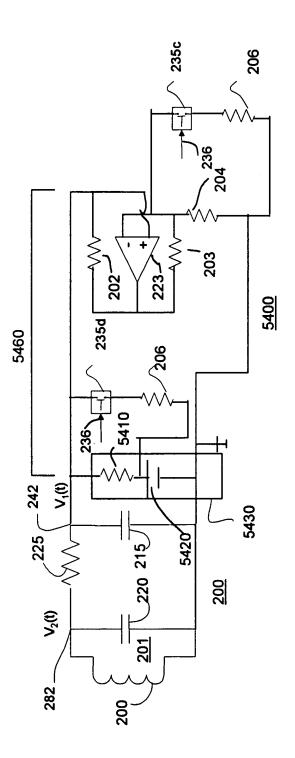


FIG. 4

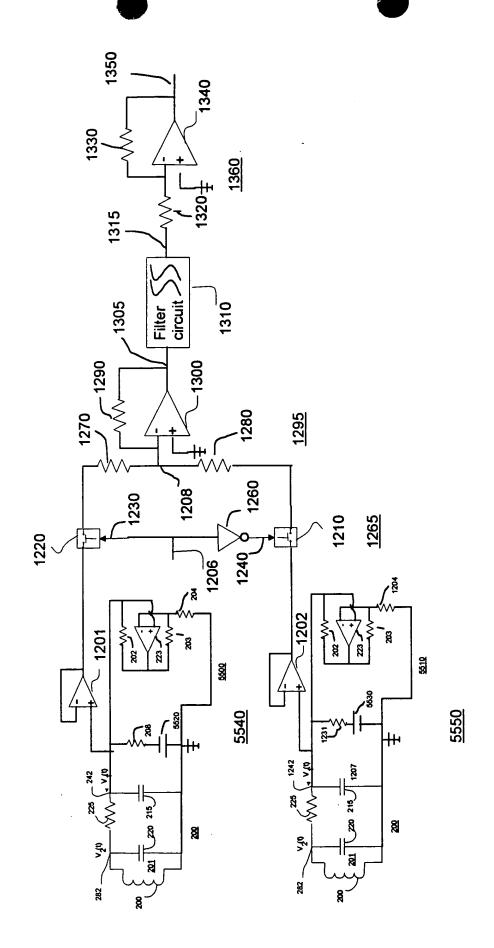


FIG. 44

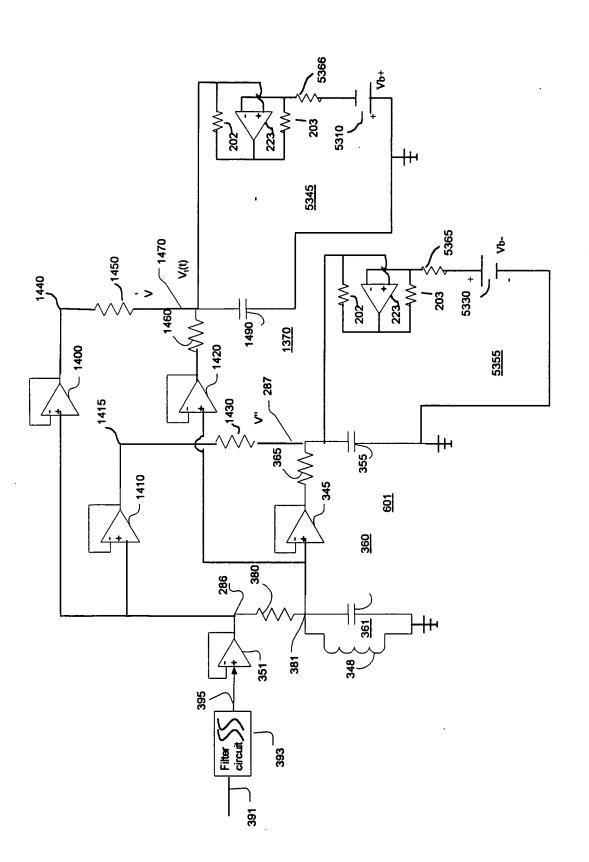


FIG. 45A

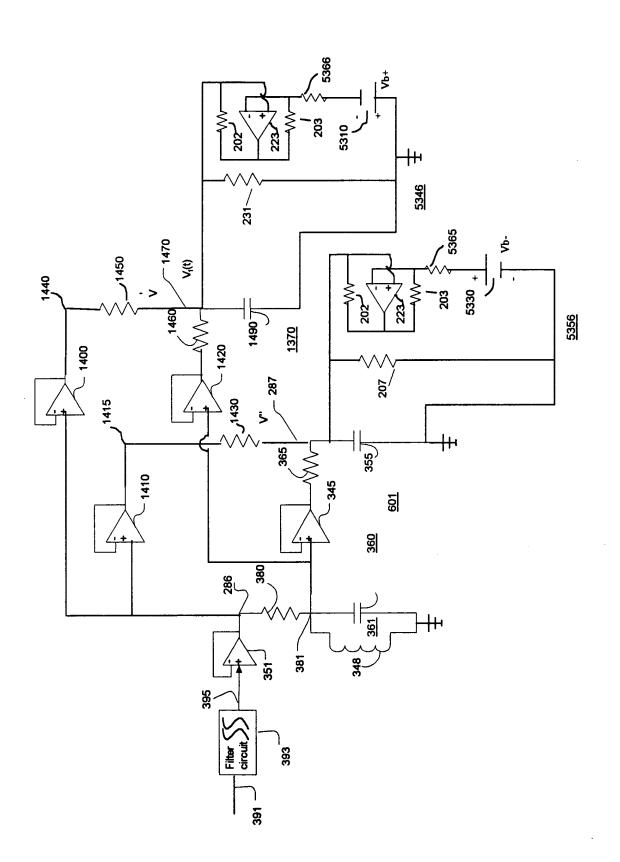


FIG. 45B

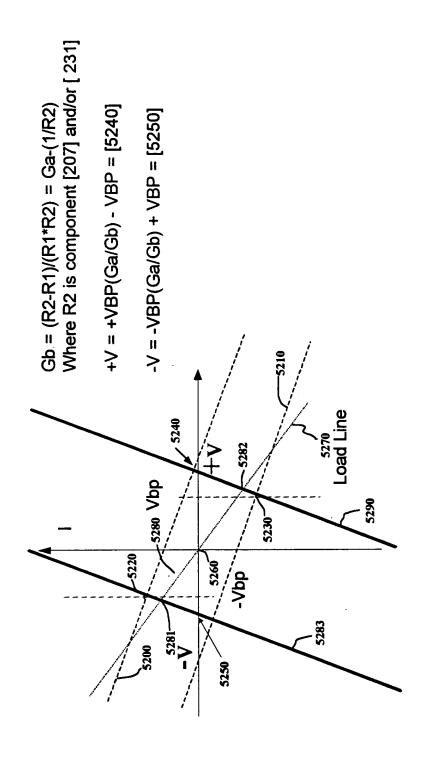


FIG. 46

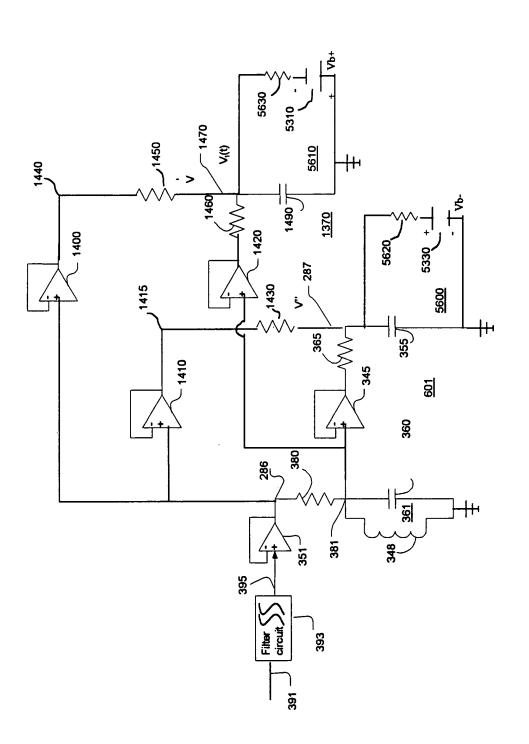


FIG. 4

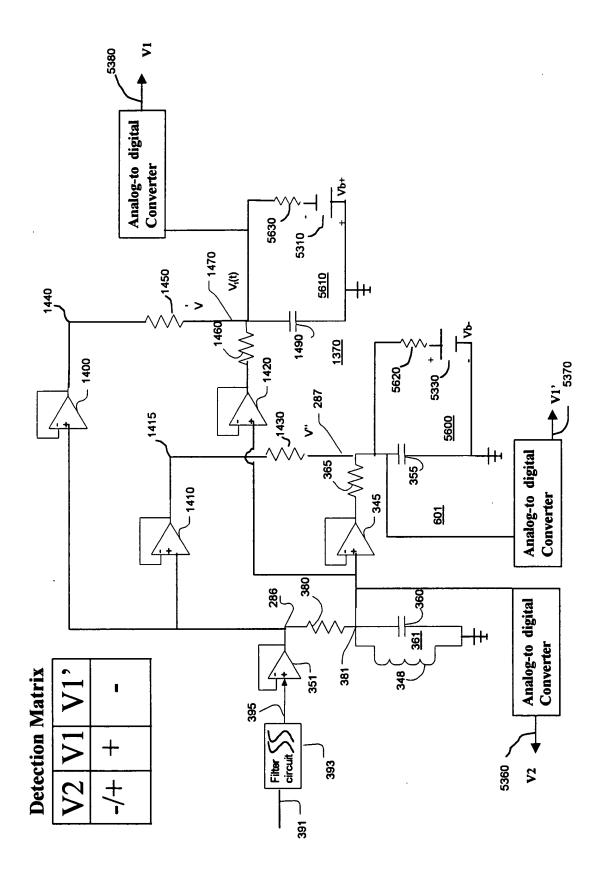


FIG. 48

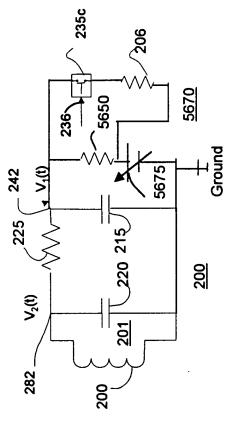


FIG. 49

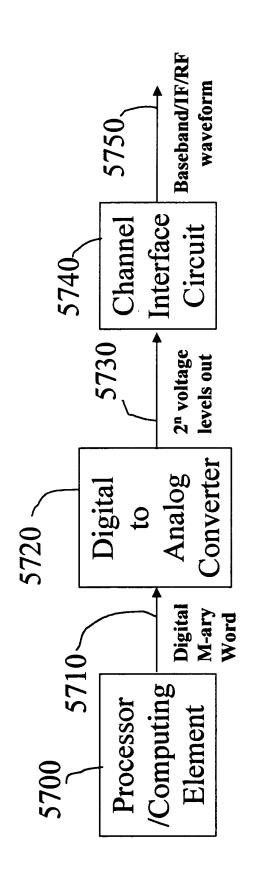


FIG. 50

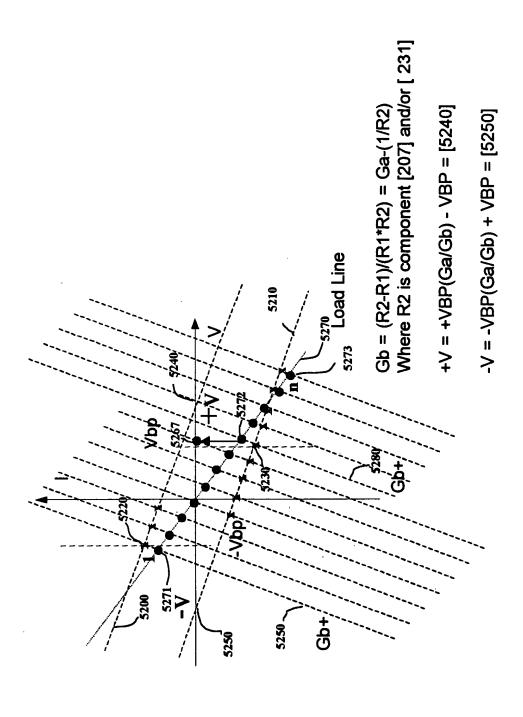
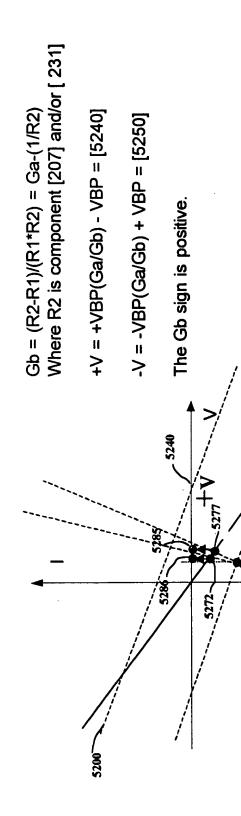


FIG. 51



Load Line

\$210 +Vbp

FIG. 52